

---

## Two or more ones

X67345\_en

---

Design a circuit that receives four inputs and produces one output. The output must be 1 when there are two or more inputs at 1. Otherwise, the output must be 0.

### Specification

```
module two_or_more_ones(a, b, c, d, out);  
  input a, b, c, d;  
  output out;
```

### Input

- *a*, *b*, *c* and *d* are the four inputs.

### Output

- *out* is the output.

### Problem information

Author: Jordi Cortadella

Generation: 2026-02-03T12:16:16.027Z

© *Jutge.org*, 2006–2026.

<https://jutge.org>