
Parity

X16400_en

Design a circuit that calculates the parity of an 8-bit input vector. The output of the circuit must be 0 if the input vector has an even number of zeros, otherwise the output must be 1.

Specification

```
module parity8(v, parity );  
  input [7:0] v;  
  output parity;
```

Input

- *v* is the 8-bit input vector.

Output

- *parity* indicates the parity of the input vector.

Problem information

Author: Jordi Cortadella

Generation: 2026-02-03T12:17:49.274Z

© *Jutge.org*, 2006–2026.

<https://jutge.org>