Mod-3 counter X05944_en

Design a circuit that implements a mod-3 counter, i.e., it outputs the sequence $0 \to 1 \to 2 \to 0 \to 1 \to 2 \to \cdots$.

Specification

```
module mod3_counter(count, clk, rst );
    input clk , rst ;
    output [1:0] count;
```

Input

- *clk* is the clock signal.
- *rst* is the synchronous reset signal.

Output

- *count* is the 2-bit output of the counter.

Problem information

Author: Jordi Cortadella

Generation: 2026-07-14T06:45:36.823Z

© *Jutge.org*, 2006–2026.
https://jutge.org